

Engineer (m/f/d) Analog Design

About Arioso Systems

Want convenient access to voice-controlled internet services anytime, anywhere? Even without the detour via cell phone? Smart hearables are to be equipped with tiny, energy-efficient, and cost-effective microspeakers to meet the increasing demands for size, functionality and price. Such silicon microspeakers are the innovation of Arioso Systems GmbH. We are a spin-off company of the Fraunhofer Institute for Photonic Microsystems in Dresden and looking for new reinforcements for our team in Dresden or Cottbus.

Our customers are all well-known manufacturers of cell phones and headphones. Our challenge is to quickly transfer a preliminary developed technology into successful products. With this we want to conquer our place in a strongly growing market. We also have very strong support from financial investors in this endeavour. For more information visit www.arioso-systems.com

We offer:

- An inspiring and family-friendly atmosphere as a high-tech start-up company under the leadership of a very experienced management team.
- Exciting research and development tasks at the interface of electroacoustics, semiconductor/microsystems technology and mechanical engineering.
- Intensive collaboration with renowned customers, research partners and manufacturers from all over the world.
- Great career opportunities focusing on technical marketing and research & development in a fast growing company.

Your profile:

- You have completed your studies in electrical engineering, electronics, mechatronics or physics with a specialisation in semiconductor technology / circuit design with very good results.
- You have good knowledge in the design, simulation and layout of integrated circuits in Cadence Virtuoso or a comparable EDA software.
- You look forward to communicating with international customers and investors.
- You have fluent German and business fluent English skills.

Your tasks:

- In close collaboration with industrial customers and our MEMS department, you will be involved in the further development of CMOS-based micro speakers.

Arioso Systems GmbH

- Based on extensive preliminary work, your work includes important contributions to the design of power-efficient ASIC drivers with analogue and digital interfaces to control our novel MEMS μ Speakers for mobile communication.
- Experimental characterisation of the developed ASICs in the laboratory.
- Development of test protocols and test procedures for quality control.
- You will be involved in the application and implementation of public funding projects.
- You will assist in patenting and publishing your results.

Interested?

We are looking forward to your application!

Please send it electronically to career@arioso-systems.com.

If you have any questions, you can reach us on +49-351-501959-0.